

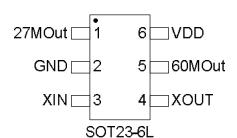
## Application Specific Quick Turn Clock<sup>™</sup> For use with Sigma Designs SMP8644 and SMP8654

PIN CONFIGURATION

#### **FEATURES**

## • Advanced Low Jitter PLL design

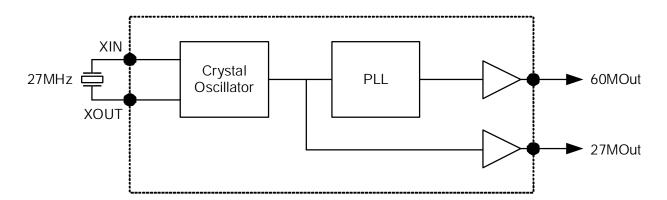
- Accepts a 27MHz Fundamental Crystal input
- Two LVCMOS Clock Outputs
  - o 27MHz
  - o 60MHz
- Single  $3.3V \pm 10\%$  power supply
- Available in 6-pin SOT Green/RoHS compliant packages



### DESCRIPTION

The PL611-01-F93 is a member of PhaseLink's Quick Turn Clock™ Family. This device has been pre-configured to supply the clocking needs of products using the Sigma Designs SMP8644 and SMP8654 Secure Media Processors. The PL611-01-F93 provides two LVCMOS clock outputs from a single 27MHz fundamental crystal input saving both board space and cost when compared to competing solutions.

## **BLOCK DIAGRAM**



#### PIN DESCRIPTION

Name	SOT-23	Туре	Description
27MOut	1	0	27MHz LVCMOS clock output
GND	2	Р	GND connection
XIN	3	I	27MHz fundamental crystal input
XOUT	4	0	27MHz fundamental crystal output
25MOut	5	0	60MHz LVCMOS clock output
VDD	6	Р	3.3V power supply



# Application Specific Quick Turn Clock<sup>™</sup> For use with Sigma Designs SMP8644 and SMP8654

## **ELECTRICAL SPECIFICATIONS**

#### **ABSOLUTE MAXIMUM RATINGS**

PARAMETERS	SYMBOL	MIN.	MAX.	UNITS
Supply Voltage Range	$V_{DD}$	-0.5	4.6	V
Input Voltage Range	Vı	-0.5	V <sub>DD</sub> +0.5	٧
Output Voltage Range	Vo	-0.5	V <sub>DD</sub> +0.5	V
Soldering Temperature (Green package)			260	°C
Storage Temperature	Ts	-65	150	°C
Ambient Operating Temperature*		-40	85	°C

Exposure of the device under conditions beyond the limits specified by Maximum Ratings for extended periods may cause permanent damage to the device and affect product reliability. These conditions represent a stress rating only, and functional operations of the device at these or any other conditions above the operational limits noted in this specification is not implied. \*Operating temperature is guaranteed by design. Parts are tested to commercial grade only.

### **GENERAL ELECTRICAL SPECIFICATIONS**

PARAMETERS	SYMBOL	CONDITIONS	MIN.	TYP.	MAX.	UNITS
Supply Current, Dynamic	I <sub>DD</sub>	Load=15pF			20	mA
Operating Voltage	$V_{DD}$		2.97		3.63	V
Output Low Voltage	V <sub>OL</sub>	$I_{OL} = +4mA$			0.4	V
Output High Voltage	V <sub>OH</sub>	I <sub>OH</sub> = -4mA	$V_{DD} - 0.4$			V
Output Current	losp	$V_{OL} = 0.4V$ , $V_{OH} = 2.4V$	10			mA
Settling Time		At power-up (V <sub>DD</sub> > 2.97V)			2	ms
Output Rise Time	t <sub>r</sub>	15pF Load, 10/90%V <sub>DD</sub>		2.5	3.5	ns
Output Fall Time	t <sub>f</sub>	15pF Load, 90/10%V <sub>DD</sub>		2.5	3.5	ns
Duty Cycle		At V <sub>DD</sub> /2	45	50	55	%

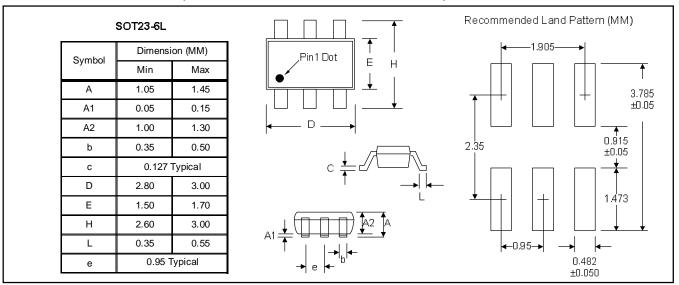
## **CRYSTAL SPECIFICATIONS**

PARAMETERS	SYMBOL	MIN.	TYP.	MAX.	UNITS
Fundamental Crystal Resonator Frequency	F <sub>XIN</sub>		27		MHz
Crystal Loading Rating	C <sub>L (xtal)</sub>		18		pF
Maximum Sustainable Drive Level				500	μW
Operating Drive Level			100		μW
Crystal Shunt Capacitance	C0			6	pF
Effective Series Resistance, Fundamental	ESR			30	Ω



## Application Specific Quick Turn Clock<sup>™</sup> For use with Sigma Designs SMP8644 and SMP8654

## PACKAGE DRAWING (GREEN PACKAGE COMPLIANT)



## ORDERING INFORMATION (GREEN PACKAGE COMPLIANT)



PhaseLink Corporation, reserves the right to make changes in its products or specifications, or both at any time without notice. The information furnished by Phaselink is believed to be accurate and reliable. However, PhaseLink makes no guarantee or warranty concerning the accuracy of said information and shall not be responsible for any loss or damage of whatever nature resulting from the use of, or reliance upon this product.

LIFE SUPPORT POLICY: PhaseLink's products are not authorized for use as critical components in life support devices or systems without the express written approval of the President of PhaseLink Corporation.